

**Name: Alok Katiyar**

**Professional Qualifications:**

- PhD (Pursuing) from Sharda University. GNoida,UP, India (Registration June 210)
- M.Tech(IT), from IP University Delhi, India in 2009
- MCA from Dr B R Ambedker University, Agra, India in 1998

**Teaching Experience (in Years): 15+ Years**

**Subjects of interest:** Pervasive Computing, Parallel Algorithms, Distributed Computing, Mobile Communication, Network Security, Unix Network Programming, Shell Scripting,

**Academic achievements:**

1. *International Journals(2)*

Alok Katiyar, Vibha Rajput, "PROACTIVE BOTTLENECK PERFORMANCE ANALYSIS IN PARALLEL COMPUTING USING OPENMP", *IJASCSE, Volume 2, Issue 5, 2013*

2. Sangeeta Kumari, Alok Katiyar, "AN ENCRYPTION FOR SECURE DATA SHARING IN PUBLIC CLOUDS USING CRYPTOGRAPHY", , *International Journal of Advanced Research in Computer Science and Software Engineering 3 (4), November - 2015, pp. 1-6*

2. *International Conferences ()*

1. Participated ACM India Annul Event, During Feb 14-16, 2014 at IIT Delhi.
2. Participated TechCon2011 organized by Delhi Technology Forum at Siri Fort, New Delhi on 22 December 2011

**Faculty Development Program/STTP/workshop Organized:**

5 Days Faculty Development program on DOT NET Framework organized during 5<sup>th</sup> May to 9<sup>th</sup> May 2014 at Lab 10, Inderprastha Engineering College, Ghaziabad. Total Participant was 30.

**Faculty Development Program/STTP/workshop attended:**

- 1) Two Week ISTE STTP on "Introduction to Design of Algorithms" organized by IIT Kharagpur during 25<sup>th</sup> May to 30<sup>th</sup> May 2015.(AICTE approved)
- 2) Participated FDP on "Big Data Analytics" by HCL info system held on 25<sup>th</sup> July 2015, at Ideal Institute of Technology, Ghaziabad.
- 3) 5 Days FDP Workshop on " Intel P-V Embedded toolkit implementation " during June 2014 at MTU Noida.(AICTE Approved)
- 4) 5 Days Faculty Workshop on "Developing .Net Application for Windows Framework" during 5<sup>th</sup> May to 9<sup>th</sup> May 2014 at IPEC, Ghaziabad.
- 5) FDP on "Instructional Design to Enhance Teaching Learning Process" During 2012-2013 at IPEC, Ghaziabad.
- 6) Attended FDP on "Advanced MATLAB Programming and Simulink" organized by Orane Info system ,SIDBI Innovation and Incubation Centre, IIT Kanpur at IPEC Ghaziabad During 09-01-2012-14-01-2012. (AICTE Approved)
- 7) Attended 6 weeks Summer School on "Parallel Computing" at Computer Center at IIT Delhi During 15<sup>th</sup> May 2011 to 30 June 2011.

**Honors and Awards Received:**

- 1) Project Consultancy of "Ambient Air Control Monitoring System" of UPPCB, Lucknow of UP Government.  
Funded Amount: 8.21 Lac.
- 2) Received 2H1 B US Visa job offer from Sun Microsystem during 2000.
- 3) Reviewer of ACM journal.
- 4) Reviewer of IJCSE, IETE Journal.
- 5) More than 10 times as Central Controller of Uttar Pradesh State Engineering Entrance Exam.
- 6) More than 8 times as University Observer during Semester Examination.
- 7) 4 times Head Examiner of University Evaluation.

- 8) Panel of Paper Setter, Board of Examiner of IETE, IGNOU, Sharda University, CSS University, Invertis University ,etc.
- 9) Taken Dissertation Viva of M.Tech at Sharda University and Amity University.
- 10) Taken Seminar at IETE Noida Centre on Algorithm Design and Analysis 08 August 2015.

**Supervisory support provided:**

*1. No. of PhD Supervision:* Completed: 0 Ongoing:

*2. No. of M Tech Thesis Guided:* Completed: 4 Submitted: 2 Ongoing: 4

**Membership of professional Societies:**

- Life member of Institute of Engineers and Telecom Engineers , New Delhi , Membership No M225105
- Advanced Computing Machinery(ACM) , Fellow Member, Membership No 4781108
- CSTA Membership No 2149334