

UTTAR PRADESH TECHNICAL UNIVERSITY LUCKNOW



SYLLABUS

Bachelor of Electrical and Electronics Engineering

3rd Year (V & VI Semester)

(Effective from Session 2015-2016)

STUDY AND EVALUATION SCHEME OF ELECTRICAL & ELECTRONICS ENGINEERING

Third Year

Semester-V

S. No	Subject Code	Name of the Subject	Periods			Evaluation Scheme			Subject Total	Credit	
			L	T	P	Sessional Assessment					ESE
						C	T	Total			
THEORY SUBJECT											
1	NEE-501	Elements Of Power System	3	1	0	30	20	50	100	150	4
2	NEE 502	Power Electronics	3	1	0	30	20	50	100	150	4
3	NEE-503	Control System	3	1	0	30	20	50	100	150	4
4	NEE-504	Microprocessor & Its Applications	3	1	0	30	20	50	100	150	4
5	NEC-508	Fundamentals of E.M. Theory	2	1	0	15	10	25	50	75	3
6	NHU-501	Engineering Economics	2	0	0	15	10	25	50	75	2
PRACTICAL/DESIGN/DRAWING											
7	NEE-551	Power Electronics Lab	0	0	3	10	10	20	30	50	1
8	NEE 552	Control System Lab	0	0	3	10	10	20	30	50	1
9	NEE-553	Microprocessor Lab	0	0	2	10	10	20	30	50	1
10	NEE 554	Simulation Based Minor Project	0	0	2	10	10	20	30	50	1
11	NGP 501	GP						50		50	1
		TOTAL	16	5	10					1000	26

STUDY AND EVALUATION SCHEME OF ELECTRICAL & ELECTRONICS ENGINEERING

Third Year

Semester-VI

S. No	Subject Code	Name of the Subject	Periods			Evaluation Scheme				Subject Total	Credit
			L	T	P	Sessional Assessment			ESE		
						C T	T A	Tot al			
THEORY SUBJECT											
1	NEE-601	Power System Analysis	3	1	0	30	20	50	100	150	4
2	NEE 602	Switchgear & Protection	3	1	0	30	20	50	100	150	4
3	NEN-603/ NEC 501	Integrated Circuits	3	1	0	30	20	50	100	150	4
4	NEN-011-NEN-014	Departmental Elective-I	3	1	0	30	20	50	100	150	4
5	NEN-021-NEN-024	Departmental Elective-II	2	1	0	15	10	25	50	75	3
6	NHU-601	Industrial Management	2	0	0	15	10	25	50	75	2
PRACTICAL/DESIGN/DRAWING											
7	NEC-651	IC Lab	0	0	2	10	10	20	30	50	1
8	NEN-652	Electrical & Electronics CAD Lab	0	0	3	10	10	20	30	50	1
9	NEN-653	Minor Project	0	0	2	10	10	20	30	50	1
10	NEN- 654	Seminar	0	0	3		50	50		50	1
11	NGP-601	GP						50		50	1
		TOTAL	16	5	10					1000	26

Elective-I

NEN – 011: Digital Control System
NEN – 012: Fundamentals of Digital Signal Processing
NEN – 013: Neural Networks and Fuzzy System
NEN – 014: Special Electrical Machine

Elective-II

NEN – 021: VLSI Design
NEN – 022: Wireless Communication
NEN – 023: Antenna and Wave Propagation
NEN – 024: Mechatronics

Annexure-III

NEE-501: ELEMENTS OF POWER SYSTEM

L T P
3 1 0

Unit-I

Power System Components:

Single line Diagram of Power system,

Brief description of power system Elements: Synchronous machine, transformer, transmission line, bus bar, circuit breaker and isolator

Supply System

Different kinds of supply system and their comparison, choice of transmission voltage

Transmission Lines:

Configurations, types of conductors, resistance of line, skin effect, Kelvin's law. Proximity effect

Unit-II

Over Head Transmission Lines

Calculation of inductance and capacitance of single phase, three phase, single circuit and double circuit transmission lines,

Representation and performance of short, medium and long transmission lines, Ferranti effect.

Surge impedance loading

Unit-III

Corona and Interference:

Phenomenon of corona, corona formation, calculation of potential gradient, corona loss, factors affecting corona, methods of reducing corona and interference.

Electrostatic and electromagnetic interference with communication lines

Overhead line Insulators:

Type of insulators and their applications, potential distribution over a string of insulators, methods of equalizing the potential, string efficiency

Unit-IV

Mechanical Design of transmission line:

Catenary curve, calculation of sag & tension, effects of wind and ice loading, sag template, vibration dampers

Insulated cables:

Type of cables and their construction, dielectric stress, grading of cables, insulation resistance, capacitance of single phase and three phase cables, dielectric loss, heating of cables.

Unit-V

Neutral grounding:

Necessity of neutral grounding, various methods of neutral grounding, earthing transformer, grounding practices

Electrical Design of Transmission Line:

Design consideration of EHV transmission lines, choice of voltage, number of circuits, conductor configuration, insulation design, selection of ground wires.

EHV AC and HVDC Transmission:

Introduction to EHV AC and HVDC transmission lines.

Text Books

- 1.W. D. Stevenson, "Element of Power System Analysis", McGraw Hill,
- 2.C. L. Wadhwa, "Electrical Power Systems" New age international Ltd. Third Edition
- 3.Asfaq Hussain, "Power System", CBS Publishers and Distributors,
- 4.B. R. Gupta, "Power System Analysis and Design" Third Edition, S. Chand & Co.
- 5.M. V. Deshpande, "Electrical Power System Design" Tata Mc Graw Hill.

Reference Books

- 6.Soni, Gupta & Bhatnagar, "A Course in Electrical Power", Dhanpat Rai & sons,
- 8.S.N.Singh, " Electric Power Generation, Transmission& distribution." PHI Learning

NEE-502:POWER ELECTRONICS

L T P
3 1 0

Unit-I

Power semiconductor Devices:

Power semiconductor devices their symbols and static characteristics, specifications of switches, types of power electronic circuits, Operation, steady state & switch characteristics & switching limits of Power Transistor Operation and steady state characteristics of Power MOSFET and IGBT

Thyristor – Operation V- I characteristics, two transistor model, methods of turn-on Operation of GTO, MCT and TRIAC

Unit-II

Power Semiconductor Devices (Contd.)

Protection of devices, Series and parallel operation of thyristors Commutation techniques of thyristor

DC-DC Converters:

Principles of step-down chopper, step down chopper with R-L load Principle of step-up chopper, and operation with RL load, classification of choppers and their various applications.

Unit-III

Phase Controlled Converters

Single phase half wave controlled rectifier with resistive and inductive loads, effect of freewheeling diode.

Single phase fully controlled and half controlled bridge converters. Performance Parameters

Three phase half wave converters, three phase fully controlled and half controlled bridge converters, Effect of source impedance Single phase and three phase dual converters

Unit-IV

AC Voltage Controllers

Principle of On-Off and phase controls

Single phase ac voltage controller with resistive and inductive loads

Three phase ac voltage controllers (various configurations and comparison only)

Single phase transformer taps changer, industrial applications.

Cyclo Converters

Basic principle of operation, single phase to single phase, three phase to single phase and three phase to three phase cyclo converters, output voltage equation and their applications.

Unit-V

Inverters

Single phase series resonant inverter, Single phase bridge inverters, Three phase bridge inverters

Voltage control of inverters, Harmonics reduction techniques, Single phase and three phase current source inverters

Text Books:

1. M.H. Rashid, "Power Electronics: Circuits, Devices & Applications", Prentice Hall of India Ltd. 3rd Edition, 2004.
2. M.D. Singh and K.B. Khanchandani, "Power Electronics" Tata MC Graw Hill, 2005
3. V.R. Moorthy, "Power Electronics : Devices, Circuits and Industrial Applications" Oxford University Press.

Reference Books:

4. M.S. Jamil Asghar, "Power Electronics" Prentice Hall of India Ltd.
5. Chakrabarti & Rai, "Fundamentals of Power Electronics & Drives" Dhanpat Rai & Sons.
6. Ned Mohan, T.M. Undeland and W.P. Robbins, "Power Electronics: Converters, Applications and Design", Wiley India Ltd, 2008.
7. S.N. Singh, "A Text Book of Power Electronics" Dhanpat Rai & Sons

NEE-503: CONTROL SYSTEM

L T P
3 1 0

Unit-I

The Control System:

Open loop & closed control; servomechanism, Physical examples. Transfer functions, Block diagram algebra, Signal flow graph, Mason's gain formula Reduction of parameter variation and effects of disturbance by using negative feedback

Unit-II

Time Response analysis:

Standard test signals, time response of first and second order systems, time response specifications, steady state errors and error constants

Design specifications of second order systems: Derivative error, derivative output, integral error and PID compensations, design considerations for higher order systems, performance indices

Unit-III

Control System Components:

Constructional and working concept of ac servomotor, synchros and stepper motor

Stability and Algebraic Criteria concept of stability and necessary conditions, Routh-Hurwitz criteria and limitations.

Root Locus Technique:

The root locus concepts, construction of root loci

Unit-IV

Frequency response Analysis: Frequency response, correlation between time and frequency responses, polar and inverse polar plots, Bode plots

Stability in Frequency Domain:

Nyquist stability criterion, assessment of relative stability: gain margin and phase margin, constant M&N circles

Unit-V

Introduction to Design:

The design problem and preliminary considerations lead, lag and lead-lag networks, design of closed loop systems using compensation techniques in time domain and frequency domain.

Review of state variable technique:

Review of state variable technique, conversion of state variable model to transfer function model and vice-versa, diagonalization, Controllability and observability and their testing.

Text Books:

1. Nagrath & Gopal, "Control System Engineering", New age International.
2. K. Ogata, "Modern Control Engineering", Prentice Hall of India.
3. B.C. Kuo & Farid Golnaraghi, "Automatic Control System" Wiley India Ltd.
4. D.Roy Choudhary, "Modern Control Engineering", Prentice Hall of India.

Reference Books:

5. Norman S. Mise, Control System Engineering , Wiley Publishing Co.
6. Ajit K Mandal, "Introduction to Control Engineering" New Age International.
7. R.T. Stefani, B.Shahian, C.J.Savant and G.H. Hostetter, "Design of Feedback Control Systems" Oxford University Press.
8. Samarjit Ghosh, "Control Systems theory and Applications", Pearson Education

UNIT-I:

Introduction to Digital Computer and Microprocessor:

Digital Computers: General architecture and brief description of elements, instruction execution, instruction format, and instruction set, addressing modes, programming system, higher level languages.

Buses and CPU Timings: Bus size and signals, machine cycle timing diagram, instruction timing, processor timing.

Microprocessor and Microprocessor Development Systems: Evolution of Microprocessor, Microprocessor architecture and its operations, memory, inputs-outputs (I/Os), data transfer schemes interfacing devices, architecture advancements of microprocessors, typical microprocessor development system.

UNIT-II:

8-bit Microprocessors.

8085 microprocessor: pin configuration, internal architecture. Timing & Signals: control and status, interrupt: ALU, machine cycles,

Instruction Set of 8085:

Addressing Modes: Register addressing, direct addressing; register indirect addressing, immediate addressing, and implicit addressing.

Instruction format, op-codes, mnemonics, no. of bytes, RTL, variants, no. of machine cycles and T states, addressing modes.

Instruction Classification: Data transfer, arithmetic operations, logical operations, branching operation, machine control; Writing assembly Language programs, Assembler directives.

UNIT-III:

16-bit Microprocessors: Architecture:

Architecture of INTEL 8086 (Bus Interface Unit, Execution unit), register organization, memory addressing, memory segmentation,

Operating Modes

Instruction Set of 8086

Addressing Modes: Instruction format:

Discussion on instruction Set: Groups: data transfer, arithmetic, logic string, branch control transfer, processor control.

Interrupts: Hardware and software interrupts, responses and types.

UNIT-IV

Fundamental of Programming: development of algorithms, flowcharts in terms of structures,(series, parallel, if-then-else etc.)

Assembler Level Programming: memory space allocation (mother board and user program) Assembler level programs (ASMs)

UNIT-V

Peripheral Interfacing:

I/O programming: Programmed I/O, Interrupt Driven I/O, DMA I/O interface: serial and parallel communication, memory I/O mapped I/Os. Peripheral Devices: 8237 DMA controller, 8255-Programmable peripheral interface, 8253/8254 Programmable timer/counter.

8259 programmable Interrupt Controller.

Text Books:

1. Gaonkar, Ramesh S, "Microprocessor Architecture, programming and applications with the 8085" Pen ram International Publishing 5th Ed.
2. Uffenbeck, John, "Microcomputers and Microprocessors" PHI/ 3rd Edition.
3. Ray, A.K. & Burchandi, K.M., "Advanced Microprocessors and Peripherals: Architecture, Programing and Interfacing" Tata Mc. Graw Hill.

4. Krishna Kant, "Microprocessors and Microcontrollers" PHI Learning.

Reference Books:

5. Brey, Barry B. "INTEL Microprocessors" Prentice Hall (India)
6. ADitya P Mathur, "Introduction to Microprocessor" Tata McGraw Hill
7. M. Rafiquzzaman, "Microprocessors- Theory and applications" PHI
8. B. Ram, "Advanced Microprocessor & Interfacing" Tata McGraw Hill
9. Renu Singh & B.P.Singh, "Microprocessor and Interfacing and applications" New Age International
10. N. Senthil Kumar, "Microprocessors and Microcontroller", Oxford University Press.
11. Liu and Gibson G.A., "Microcomputer Systems: The 8086/8088 Family" Prentice Hall (India)

NEC-508: FUNDAMENTALS OF E.M.THEORY

L T P
2 1 0

Unit I

Review of Vector analysis, Rectangular, Cylindrical and Spherical coordinates and their transformation, divergence, gradient and curl in different coordinate systems, Electric field intensity, Electric Flux density, Energy and potential.

Unit-II

Current and conductors, Dielectrics and capacitance, Poisson's and Laplace's equations.

Unit-III

Steady magnetic field, magnetic forces, materials and inductance, Time varying field and Maxwell's equation.

Unit-IV

Uniform Plane waves, Plane wave reflection and dispersion

Text Books:

1. Hayt, W.H. and Buck, J.A., "Engineering Electromagnetic" Tata McGraw Hill Publishing
2. Mathew Sadiku, "Electromagnetic Field Theory", Oxford University Press.

Reference Books:

3. Jordan E.C. and Balmain K.G., "Electromagnetic Wave and radiating Systems" Prentice Hall International, 2nd Edition.
4. Kraus, F. "Electromagnetic" Tata Mc. Graw Hill 5th Edition.
5. Ramo S, Whinnery T.R. and Vanduzer T, "Field and Waves in Communication Electronics" John Wiley and Sons 3rd Edition

NEE-551: POWER ELECTRONICS LABORATORY

L T P
0 0 3

Note: The minimum of 10 experiments is to be performed out of which at least three should be software based.

1. To study V-I characteristics of SCR and measure latching and holding currents.
2. To study UJT trigger circuit for half wave and full wave control.
3. To study single-phase half wave controlled rectified with (i) resistive load (ii) inductive load with and without free wheeling diode.
4. To study single phase (i) fully controlled (ii) half controlled bridge rectifiers with resistive and inductive loads.
5. To study three-phase fully/half controlled bridge rectifier with resistive and inductive loads.
6. To study single-phase ac voltage regulator with resistive and inductive loads.
7. To study single phase cyclo-converter
8. To study triggering of (i) IGBT (ii) MOSFET (iii) power transistor
9. To study operation of IGBT/MOSFET chopper circuit
10. To study MOSFET/IGBT based single-phase series-resonant inverter.
11. To study MOSFET/IGBT based single-phase bridge inverter.

Software based experiments(PSPICE/MATLAB)

12. To obtain simulation of SCR and GTO thyristor.
13. To obtain simulation of Power Transistor and IGBT.
14. To obtain simulation of single phase fully controlled bridge rectifier and draw load voltage and load current waveform for inductive load.
15. To obtain simulation of single phase full wave ac voltage controller and draw load voltage and load current waveforms for inductive load.
16. To obtain simulation of step down dc chopper with L-C output filter for inductive load and determine steady-state values of output voltage ripples in output voltage and load current.
- 17.

Text/Reference Books:

1. M.H.Rashid, "Power Electronics: Circuits, Devices and Applications", 3rd Edition, prentice Hall of India.
2. D.W. Hart, "Introduction to power Electronics" Prentice hall Inc.
3. Randal Shaffer, "Fundamentals of Power Electronics with MATLAB" Firewall Media,

NEE– 552: CONTROL SYSTEM LABORATORY

L T P
0 0 2

Note: The minimum of 10 experiments are to be performed from the following, out of which at least three should be software based.

1. To determine response of first order and second order systems for step input for various values of constant 'K' using linear simulator unit and compare theoretical and practical results.
2. To study P, PI and PID temperature controller for an oven and compare their performance.
3. To study and calibrate temperature using resistance temperature detector (RTD)
4. To design Lag, Lead and Lag-Lead compensators using Bode plot.
5. To study DC position control system
6. To study synchro-transmitter and receiver and obtain output vs input characteristics
7. To determine speed-torque characteristics of an ac servomotor.
8. To study performance of servo voltage stabilizer at various loads using load bank.
9. To study behavior of separately excited dc motor in open loop and closed loop conditions at various loads.

Software based experiments (Use MATLAB, LABVIEW software etc.)

10. To simulate PID controller for transportation lag.
11. To determine time domain response of a second order system for step input and obtain performance parameters.
12. To convert transfer function of a system into state space form and vice-versa.
13. To plot root locus diagram of an open loop transfer function and determine range of gain 'k' for stability.
14. To plot a Bode diagram of an open loop transfer function.
15. To draw a Nyquist plot of an open loop transfer functions and examine the stability of the closed loop system.

Reference Books:

1. K.Ogata, "Modern Control Engineering" Prentice Hall of India.
2. Norman S.Nise, "Control System Engineering", John Wiley & Sons.
3. M.Gopal, "Control Systems: Principles & Design" Tata Mc Graw Hill.

NEE-553: MICROPROCESSOR LABORATORY

L T P
0 0 2

A. Study Experiments

1. To study 8085 based microprocessor system
2. To study 8086 and 8086A based microprocessor system
3. To study Pentium Processor

B. Programming based Experiments (any four)

4. To develop and run a program for finding out the largest/smallest number from a given set of numbers.
5. To develop and run a program for arranging in ascending/descending order of a set of numbers
6. To perform multiplication/division of given numbers
7. To perform conversion of temperature from $^{\circ}\text{F}$ to $^{\circ}\text{C}$ and vice-versa
8. To perform computation of square root of a given number
9. To perform floating point mathematical operations (addition, subtraction, multiplication and division)

C. Interfacing based Experiments (any four)

10. To obtain interfacing of RAM chip to 8085/8086 based system
11. To obtain interfacing of keyboard controller
12. To obtain interfacing of DMA controller
13. To obtain interfacing of PPI
14. To obtain interfacing of UART/USART
15. To perform microprocessor based stepper motor operation through 8085 kit
16. To perform microprocessor based traffic light control
17. To perform microprocessor based temperature control of hot water.

NEE-601: POWER SYSTEM ANALYSIS

L T P
3 1 0

Unit-I

Representation of Power System Components:

Synchronous machines, Transformers, Transmission lines, One line diagram, Impedance and reactance diagram, per unit System

Symmetrical components:

Symmetrical Components of unbalanced phasors, power in terms of symmetrical components, sequence impedances and sequence networks.

Unit-II

Symmetrical fault analysis:

Transient in R-L series circuit, calculation of 3-phase short circuit current and reactance of synchronous machine, internal voltage of loaded machines under transient conditions

Unsymmetrical faults:

Analysis of single line to ground fault, line-to-line fault and Double Line to ground fault on an unloaded generators and power system network with and without fault impedance.

Formation of Z_{bus} using singular transformation and algorithm, computer method for short circuit calculations

Unit-III Load Flows:

Introduction, bus classifications, nodal admittance matrix (Y_{BUS}), development of load flow equations,

load flow solution using Gauss Siedel and Newton-Raphson method, approximation to N-R method, line flow equations and fast decoupled method

Unit-IV

Power System Stability:

Stability and Stability limit, Steady state stability study, derivation of Swing equation, transient stability studies by equal area criterion and step-by-step method. Factors affecting steady state and transient stability and methods of improvement

Unit-V Traveling Waves:

Wave equation for uniform Transmission lines, velocity of propagation, surge impedance, reflection and transmission of traveling waves under different line loadings. Bewlay's lattice diagram, protection of equipments and line against traveling waves.

Text Books:

1. W.D. Stevenson, Jr. "Elements of Power System Analysis", Mc Graw Hill.
2. C.L. Wadhwa, "Electrical Power System", New Age International.
3. Chakraborty, Soni, Gupta & Bhatnagar, "Power System Engineering", Dhanpat Rai & Co.
4. T.K Nagsarkar & M.S. Sukhija, "Power System Analysis" Oxford University Press, 2007.

Reference Books:

5. O.I. Elgerd, "Electric Energy System Theory" Tata McGraw Hill.
6. Hadi Sadat; "Power System Analysis", Tata McGraw Hill.
7. D.Das, "Electrical Power Systems" New Age International.
8. J.D. Glover, M.S. Sharma & T.J. Overbye, "Power System Analysis and Design" Thomson.
9. P.S.R. Murthy "Power System Analysis" B.S. Publications.
10. Stagg and El-Abiad, "Computer Methods in Power System Analysis" Tata Mc Graw Hill
11. Kothari & Nagrath, "Modern Power System Analysis" Tata Mc. Graw Hill.

NEE – 602: SWITCHGEAR AND PROTECTION

L T P
3 1 0

Unit I:

Introduction to Protection System:

Introduction to protection system and its elements, functions of protective relaying, protective zones, primary and backup protection, desirable qualities of protective relaying, basic terminology.

Relays:

Electromagnetic, attracted and induction type relays, thermal relay, gas actuated relay, design considerations of electromagnetic relay.

Unit-II:

Relay Application and Characteristics:

Amplitude and phase comparators, over current relays, directional relays, distance relays, differential relay

Static Relays:

Comparison with electromagnetic relay, classification and their description, over current relays, directional relay, distance relays, differential relay.

Unit-III

Protection of Transmission Line:

Over current protection, distance protection, pilot wire protection, carrier current protection, protection of bus, auto re-closing,

Unit-IV:

Circuit Breaking:

Properties of arc, arc extinction theories, re-striking voltage transient, current chopping, resistance switching, capacitive current interruption, short line interruption, circuit breaker ratings.

Testing Of Circuit Breaker:

Classification, testing station and equipments, testing procedure, direct and indirect testing

Unit-V

Apparatus Protection:

Protection of Transformer, generator and motor.

Circuit Breaker:

Operating modes, selection of circuit breakers, constructional features and operation of Bulk Oil, Minimum Oil, Air Blast, SF₆, Vacuum and d. c. circuit breakers.

Text Books:

1. S. S. Rao, "Switchgear and Protection", Khanna Publishers.
2. B. Ravindranath and M. Chander, Power system Protection and Switchgear, Wiley Eastern Ltd.

Reference Books:

3. B. Ram and D. N. Vishwakarma, "Power System Protection and Switchgear", Tata Mc. Graw Hill
4. Y. G. Paithankar and S R Bhide, "Fundamentals of Power System Protection", Prentice Hall of India.
5. T.S.M Rao, "Power System Protection: Static Relays with Microprocessor Applications" Tata Macgraw Hill".
6. A.R. Van C. Warringtaon , " Protective Relays- Their Theory and Practice, Vol. I & II" Jhon Willey & Sons.

EEC 501 INTEGRATED CIRCUITS

Unit-I

Analog Integrated circuit Design: an overview: Current Mirrors using BJT and MOSFETs, Simple current Mirror, Base current compensated current Mirror, Wilson and Improved Wilson Current Mirrors, Widlar Current source and Cascode current Mirror.

The 741 IC Op-Amp: Bias circuit, short circuit protection circuitry, the input stage, the second stage, the output stage, and device parameters; DC Analysis of 741: Small Signal Analysis of input stage, the second stage, the output stage; Gain, Frequency Response of 741; a Simplified Model, Slew Rate, Relationship Between f_t and SR. 8

Unit-II

Linear Applications of IC op-amps: An Overview of Op-Amp (ideal and non ideal) based Circuits V-I and I-V converters, generalized Impedance converter, simulation of inductors.

Filters: First and second order LP, HP, BP BS and All pass active filters, KHN, Tow-Thomas and State Variable Biquad filters; Sinusoidal oscillators. 8

Unit-III

Digital Integrated Circuit Design-An Overview: CMOS Logic Gate Circuits: Basic Structure CMOS realization of Inverters, AND, OR, NAND and NOR Gates.

Latches and Flip flops: The Latch, The SR Flip-flop, CMOS Implementation of SR Flip-flops, A Simpler CMOS Implementation of the Clocked SR Flip-flop, D Flip-flop Circuits. 8

Unit-IV

Non-Linear applications of IC Op-amps: Log–Anti Log Amplifiers, Precision Rectifiers, Peak Detectors, Simple and Hold Circuits, Analog Multipliers and their applications. Op-amp as a comparator, Zero crossing detector, Schmitt Trigger, Astable multivibrator, Monostable multivibrator, Generation of Triangular Waveforms. 8

Unit-V

D/A and A/D converters Integrated Circuit Timer: The 555 Circuit, Implementing a Monostable Multivibrator Using the 555 IC, Astable Multivibrator Using the 555 IC.

Phase locked loops (PLL): Ex-OR Gates and multipliers as phase detectors, Block Diagram of IC PLL, Working of PLL and Applications of PLL. 8

Text Book:

[1] Sedra and Smith, “Microelectronic Circuits”, Oxford University Press.

Reference Books:

[2] Michael Jacob, `Applications and Design with Analog Integrated Circuits`, PHI, 2006.

[3] Jacob Milliman and Arvin Gabel, “Microelectronics”, TMH, 2008.

DEPARTMENTAL ELECTIVES

ELECTIVE – I

NEE – 011/NEN-011: Digital Control System

L T P
3 1 0

UNIT-I

Signal Processing in Digital Control:

Basic digital control system, advantages of digital control and implementation problems, basic discrete time signals, z-transform and inverse z-transform, modeling of sample- hold circuit., pulse transfer function, solution of difference equation by z-Transform method.

UNIT-II

Design of Digital Control Algorithms:

Steady state accuracy, transient response and frequency response specifications, digital compensator design using frequency response plots and root locus plots.

UNIT-III

State Space Analysis and Design:

State space representation of digital control system, conversion of state variable models to transfer functions and vice versa, solution of state difference equations, controllability and observability, design of digital control system with state feedback.

UNIT-IV

Stability of Discrete System:

Stability on the z-plane and Jury stability criterion, bilinear transformation, Routh stability criterion on rth plane.

Lyapunov's Stability in the sense of Lyapunov, stability theorems for continuous and discrete systems, stability analysis using Lyapunov's method.

UNIT-V

Optimal digital control :

Discrete Euler Lagrange equation, max. min. principle, optimality & Dynamic programming, Different types of problem and their solutions.

Text Books:

1. B.C.Kuo, "Digital Control System",Saunders College Publishing.
2. M.Gopal, "Digital Control and State Variable Methods", Tata McGraw Hill.

Reference Books:

3. J.R.Leigh, "Applied Digital Control", Prentice Hall, International
4. C.H. Houpis and G.B.Lamont, "Digital Control Systems:Theory, hardware, Software",Mc Graw Hill.

NEE – 012/NEN-012: FUNDAMENTALS OF DIGITAL SIGNAL PROCESSING

L T P
3 1 0

Unit-I

Discrete-Time Signals And Systems:

Sequences, discrete time systems, LTI systems, frequency domain representation of discrete time signals and systems, discrete time signals and frequency domain representation, Fourier Transform.

Discrete Fourier Transform:

Discrete Fourier transforms, properties, linear convolution using DFT, DCT

Unit-II

Sampling of Continuous Time Signals:

Sampling and reconstruction of signals, frequency domain representation of sampling, discrete time processing of continuous time signals, continuous time processing of discrete time signals, changing the sampling rate using discrete time processing, multi rate signal processing, digital processing of analog signals, over sampling and noise shaping in A/D and D/A conversion

Unit-III

Transform Analysis of LTI Systems:

Frequency response of LTI systems, system functions, frequency response for rational system functions, magnitude-phase relationship, all pass systems, minimum phase systems, and linear systems with generalized linear phase

Overview of finite precision numerical effects, effects of coefficient quantization, Effects of round-off noise in digital filters, zero-input limit cycles in fixed point realizations of IIR digital filters.

Unit-IV

Filter Design Techniques:

Design of D-T IIR filters from continuous – time filters, design of FIR filters by windowing, Kaiser Window method, optimum approximations of FIR filters, FIR equiripple approximation

Unit-V

Efficient computation of the DFT:

Goertzel algorithm, decimation in time and decimation in frequency, FFT algorithm, practical considerations, implementation of the DFT using convolution, effects of finite register length.

Fourier Analysis of Signals Using DFT :

DFT analysis of sinusoidal signals, time-dependent Fourier transforms: Block convolution, Fourier analysis of non – stationary and stationary random signals, spectrum analysis of random signals using estimates of the autocorrelation sequence

Text Books:

1. S. Salivahanan, “Digital Signal Processing”, McGraw Hill Education (India) Private Limited.
2. Oppenheim A.V., Schafer, Ronald W. & Buck, John R, ”Discrete Time Signal processing”, Pearson Education .

Reference Books:

3. Proakis, J.G. & Manolakis, D.G.,” Digital Signal Processing: Principles Algorithms and Applications”, Prentice Hall of India.
4. Rabiner, L.R. and Gold B., “Theory and applications of DSP”, Prentice Hall of India.
5. Oppenheim, Alan V. & Willsky, Alan S. , “Signals and Systems” , Prentice Hall of India, 2nd Edition
6. Johnson, J.R. , “Introduction to Digital Signal Processing”, Prentice Hall of India.

NEE – 013/NEN-013: NEURAL NETWORKS AND FUZZY SYSTEM

L T P
3 1 0

Unit-I

Neural Networks-1(Introduction & Architecture)

Neuron, Nerve structure and synapse, Artificial Neuron and its model, activation functions, Neural network architecture: single layer and multilayer feed forward networks, recurrent networks. Various learning techniques; perception and convergence rule, Auto-associative and hetro-associative memory

Unit-II

Neural Networks-II (Back propogation networks)

Architecture: perceptron model, solution, single layer artificial neural network, multilayer perception model; back propogation learning methods, effect of learning rule co-efficient ;back propogation algorithm, factors affecting backpropagation training, applications.

Unit-III

Fuzzy Logic-I (Introduction)

Basic concepts of fuzzy logic, Fuzzy sets and Crisp sets, Fuzzy set theory and operations, Properties of fuzzy sets, Fuzzy and Crisp relations, Fuzzy to Crisp conversion.

Unit-IV

Fuzzy Logic –II (Fuzzy Membership, Rules)

Membership functions, interference in fuzzy logic, fuzzy if-then rules, Fuzzy implications and Fuzzy algorithms, Fuzzyfications & Defuzzificataions, Fuzzy Controller, Industrial applications.

Unit-V

Fuzzy Neural Networks:

L-R Type fuzzy numbers, fuzzy neutron, fuzzy back propogation (BP), architecture, learning in fuzzy BP, inference by fuzzy BP, applications.

Text Books:

1. Kumar Satish, “Neural Networks” Tata Mc Graw Hill
2. S. Rajsekaran & G.A. Vijayalakshmi Pai, “Neural Networks, Fuzzy Logic and Genetic Algorithm: Synthesis and Applications” Prentice Hall of India.

Reference Books:

3. Siman Haykin, “Neural Netowrks” Prentice Hall of India
4. Timothy J. Ross, “Fuzzy Logic with Engineering Applications” Wiley India.

NEE-603/NEN -014: SPECIAL ELECTRICAL MACHINE

L T P

3 1 0

UNIT-I

Poly-phase AC Machines:

Construction and performance of double cage and deep bar three phase induction motors; e.m.f. injection in rotor circuit of slip ring induction motor, concept of constant torque and constant power controls, static slip power recovery control schemes (constant torque and constant power)

UNIT-II

Single phase Induction Motors:

Construction, starting characteristics and applications of split phase, capacitor start, capacitor run, capacitor-start capacitor-run and shaded pole motors.

Two Phase AC Servomotors:

Construction, torque-speed characteristics, performance and applications.

UNIT-III Stepper Motors:

Principle of operation, variable reluctance, permanent magnet and hybrid stepper motors, characteristics, drive circuits and applications.

Switched Reluctance Motors:

Construction; principle of operation; torque production, modes of operation, drive circuits.

UNIT-IV

Permanent Magnet Machines:

Types of permanent magnets and their magnetization characteristics, demagnetizing effect, permanent magnet dc motors, sinusoidal PM ac motors, brushless dc motors and their important features and applications, PCB motors.

Single phase synchronous motor; construction, operating principle and characteristics of reluctance and hysteresis motors; introduction to permanent magnet generators and applications

UNIT-V

Single Phase Commutator Motors:

Construction, principle of operation, characteristics of universal and repulsion motors ; Linear Induction Motors. Construction, principle of operation, Linear force, and applications.

Text Books:

1. P.S. Bimbhra “Generalized Theory of Electrical Machines” Khanna Publishers.
2. P.C. Sen “ Principles of Electrical Machines and Power Electronics” John Willey & Sons, 2001
3. G.K. Dubey “Fundamentals of Electric Drives” Narosa Publishing House, 2001

Reference Books:

4. Cyril G. Veinott “Fractional and Sub-fractional horse power electric motors” McGraw Hill International, 1987
5. M.G. Say “ Alternating current Machines” Pitman & Sons .

DEPARTMENTAL ELECTIVES
ELECTIVE – II

NEN-021 : VLSI DESIGN

L T P
2 1 0

UNIT-I

Introduction to integrated circuit technology. CMOS fabrication, the p-well process, n-well process, twin tub process. Bi-CMOS technology. Basic electrical properties of MOS circuits, I_{ds} - V_{ds} relationship, MOS transistor threshold voltage V_t , Transconductance and output conductance, MOS transistor figure of merit.

UNIT-II

The n-MOS inverter, pull-up to pull-down ratio, CMOS inverter and its characteristics, latch –up in CMOS circuits, stick diagrams, n-MOS design style, CMOS design style, lambda based design rules, Body effect, sheet resistance, capacitances of layers, Gate delays, Delay estimation, logical efforts, Scaling models and scaling factors, limitation of scaling, Limits of miniaturization.

UNIT-III

n-MOS, CMOS NAND Gates, n-MOS, CMOS NOR gates. Combinational circuit design, sequential circuit design, design considerations, problems associated with VLSI Design, Design Methodology and Tools, Standard Cell Based Design, Design Flows, Automated Layout Generation, Placement, Floor planning, Routing, Parasitic Extraction, Timing Analyses.

UNIT-IV

Full Custom Design, Semi Custom Design, Programmable Logic structures, Field Programmable Gate arrays (FPGA), Configurable Logic Block (CLB), Application-Specific Integrated Circuits (ASICs)

UNIT-V

Design for Testability, Faults types and Models, Controllability and Observability, AD HOC Design Techniques, Scan-Based Techniques, Built-In self Test (BIST), Techniques, Current Monitoring IDDQ Test. Packaging, Package Parasitic, Heat dissipation, Design Economics, Parametric yield.

Text Books:

1. Basic VLSI Design by Douglas A. Pucknell & Kamran Eshraghian, Prentice-Hall of India.
2. CMOS VLSI Design, A Circuits and Systems Perspective by Neil H.E. Weste, David Harris, Ayan Banerjee, Pearson Education.
3. CMOS Digital Integrated Circuits Analysis and Design by Sung-Mo Kang, Yusuf Leblebici. Tata Mc-Graw-Hill.

References:

4. Digital Integrated Circuits A Design Perspective by Jab M. Rabaey, Anantha Chandra kasan, Borivoje Nikolic, Prentice-Hall of India Pvt. Limited.
5. Principles of C-MOS VLSI Design A systems Perspective by Neil H.E. Weste, Kamrau Eshraghian, Pearson Education
6. Application-Specific Integrated Circuits by Michal John Sebastian smith, Pearson Education..

NEN -022: WIRELESS COMMUNICATION

L T P

2 1 0

Unit-I

Evolution of mobile radio communication fundamentals. Large scale path loss: propagation models, reflection, diffraction, scattering, practical link budget design using path loss model. Small scale fading & multi-path propagation and measurements, impulse response model and parameters of multi-path channels, types of fading, theory of multi-path shape factor for fading wireless channels.

Unit-II

Spread spectrum modulation techniques: Pseudo-noise sequence, direct sequence spread spectrum (DS-SS), frequency hopped spread spectrum (FH-SS), performance of DS-SS, performance of FH-SS, modulation performance in fading and multi-path channels, fundamentals of equalization, equalizer in communication receiver, survey of equalization techniques, linear equalizer, linear equalizer, non-linear equalizations, diversity techniques, RAKE receiver.

Unit-III

Characteristics of speech signals, quantisation techniques, vocoders, linear predictive coders, time division multiple access, space division multiple access, and frequency division multiple access.

Unit-IV

Frequency reuse, channel assignment strategies, handoff strategies, interference and system capacity, improving coverage and capacity in cellular systems.

Text Book:

1. T.S. Rappaport, "Wireless Communication-Principles and practice", Pearson

Reference Books:

1. William C. Y. Lee, "Mobile communication Design and fundamentals"
2. D. R. Kamillo Fehar, "Wireless digital communication"
3. Haykin S & Moher M., "Modern wireless communication", Pearson, 2005
4. R. Pandya, "Mobile and personal communication system", PHI.

NEN -023 ANTENA AND WAVE PROPOGATION

L T P
2 1 0

UNIT-I

Antena Principles: Potential functions & Electromagnetic field, Current Elements, Radiation from Monopole & Half Wave Dipole, power radiated by current element, radiation resistance.

Network Theorems

Directional Properties of Dipole Antenna.

Antenna Gain, Effective Area, Antenna Terminal impedance, Practical Antennas and Methods of Excitation, Antenna Temperature and Signal. To Noise Ratio.

UNIT-II

Antenna Arrays: Two Element Array, Horizontal Patterns in Broadcast Arrays, Linear Arrays, Multiplication of Patterns, effect of the earth on vertical patterns, Binomial array.

UNIT-III

Wave Propagation: Modes of Propagation, Plane Earth Reflection. Space wave and Surface Wave, Reflection and refraction waves by the lonosphere Tropospheric Wave.

Ionosphere Wave Propagation in the Inosphere , Virtual Height , MUF Critical frequency, Skip Distance, Duct Propagation, Space wave.

UNIT-IV

Practical Antenas:

VLF and LF tranmitting antennas, effect of antenna height, Field of short dipole, electric field of small loop antenna, Directivity of circular loop antenna with uniform current, Directivity of Circular loop antenna with uniform current, Yagi-Uda array: Square corner yagi-uda hybride, circular polarization

Rhombic Antenna Weight and Leg length

Parabolic Reflectors Properties, Comparison with corner reflectors

Horn Antenna: Length and Aperture.

Introduction to Turstile Antenna

Effect of ground on antenna performance.

Broadband Antenna: Frequency independent concept, RUMSEY'Ss Principle, Frequency independent planar log spiral antenna, Frequency independent conical spiral Antenna.

Text Books:

1. Jordan Edwards C. and Balmain Keith G./ "Electromagnetic Waves and Radiating Systems"/ Prentice Hall (India)
2. Kraus, JohnD & Mashefka, Ronald J. / "Antennas: For All Applications" / Tata McGraw Hill, 3rd Ed.

Reference Books:

1. Prasad, K.D./ "Antennas and Wave Propagation" /Khanna Publications.
2. Collin, R./ "Antennas and Radiowave Propagation" /Tata McGraw-Hill
3. Hayt Jr. William H./ "Engineering Electromagnetics"/Tata McGraw-Hill.
4. Das, Annaparna & Das, Sisir K. / "Microwave Engineering"/Tata McGraw Hill.
5. Roy, Sitesh Kumar & Mitra, Monojit / "Microwave Semiconductor Devices" / Prentice Hall (India).

1. Mechatronics and its scope:

Sensors and transducers- Displacement, position & proximity, velocity, force, pressure and level. Signal conditioning amplification, filtering & data acquisition.

2. Pneumatic and Hydraulic actuation systems:

Directional control valves, pressure control valves and cylinders. process control valves. Mechanical

actuation system-kinematic chains, cams, gear trains. Ratchet & Pawl, dampers, bearings. Electrical actuation system. Mechanical switches- solenoid operated solid state switches, DC, AC & stepper motors.

Building blocks of Mechanical spring, mass and damper. Drives- Electrical Drives, Fluid systems, hydraulic, servo, closedloop controllers.

3. Elements of Microprocessors & Microcontrollers, Programmable logic controllers & Communication interface.

4. Case Studies of Mechatronic Systems:

Industrial Robot and its control
Automobile Engine Control
Electromechanical disc-control.

5. Vehicle suspension Control:

Micro mechanical systems. Computer Printer, VCR, Fax Machine, NC Machine.

References:

1. Rolf Isenmann, " Mechatronics Systems", Springer, 2005.
2. W. Bolten, "Mechatronics", Pearson Education 2003.
3. HMT Ltd, "Mechatronics:", Tata McGraw Hill 1998.

NEC– 651: IC LAB

L T P
0 0 3

INTEGRATED CIRCUITS LAB

Objective: - To design and implement the circuits to gain knowledge on performance of the circuit and its application. These circuits should also be simulated on Pspice.

1. Log and antilog amplifiers.
2. Voltage comparator and zero crossing detectors.
3. Second order filters using operational amplifier for:
 - a. Low pass filter of cutoff frequency 1 KHz.
 - b. High pass filter of frequency 12 KHz.
 - c. Band pass filter with unit gain of pass band from 1 KHz to 12 KHz.
4. Wien bridge oscillator using operational amplifier.
5. Determine capture range; lock in range and free running frequency of PLL.
6. Voltage regulator using operational amplifier to produce output of 12V with maximum load current of 50 mA.
7. A/D and D/A convertor.
8. Voltage to current and current to voltage convertors.
9. Function generator using operational amplifier (sine, triangular & square wave)
10. Astable and monostable multivibrator using IC 555.

NEN=652: ELECTRICAL and ELECTRONICS CAD LAB

L T P
0 0 3

1. Design of Single phase transformer.
2. Design of Single phase Induction Motor.
3. Design of DC motor.
4. Design of DC generator.
5. Design of Single phase alternator.
6. Design of Synchronous Motor.
7. Design of lag, lead and lag-lead compensator.
8. Design of PI & PD Controller.
9. Design of PID controller.
10. Design of Analog Filter
11. Design of Self-tuned filter.
12. Design of voltage controller Oscillator
13. Design of DC-DC converter.

Text Books:-

1. A.K. Sawhney, "A Course in Electrical Machine Design" Dhanpat Rai & Sons.
2. M.G. Say, "The Performance and Design of AC Machines" Pitman & Sons.